

TTP Development Cluster

The Complete TTP System



TTP Development Cluster is a complete and ready-to-run system for TTP prototyping and development projects. TTP Development Cluster comprises the complete TTP software and hardware configuration package. It contains all the TTP tools and embedded software that developers need to work with time-triggered communication architecture. The toolset is designed for efficient and intuitive software development. Full on-site setup and quick start are included.

Hardware

TTP Development Cluster hardware is based on TTP Powerlinks – or optionally TTP Pownodes - mounted on a rack and with one TTP Monitoring Node for real-time TTP bus monitoring and download. Each TTP Powerlink/TTP Pownode is equipped with an austriamicrosystems AS8202NF TTP communication controller. In addition to TTP, a broad variety of interfaces is supported: CAN, digital I/O, and analog inputs. TTP Development Cluster can be ordered in configurations from 4 to 64 nodes.

The package also includes a one-day on-site quick start; all the hardware and software is installed and made ready to go for development.

KEY FEATURES/BENEFITS

- Ready-to-go TTP system
- All necessary TTP software included
- Demo application included
- Includes on-site setup and quick start
- Real-time monitoring of TTP network
- Application loading over the TTP network
- Fast evaluation of TTP
- Supports time-triggered architecture

Software

TTP Development Cluster includes all necessary TTP Tools and embedded software.

The system engineer defines the network and communication requirements using TTP Plan. Based on these requirements, TTP Plan creates the network schedule.

In TTP Build the node designer provides additional implementation-specific information and generates the configuration tables for the communication layer and the MEDL.

TTP Load can quickly download new software to all the nodes in the system. TTP View provides real-time monitoring of the network.

Hardware Specifications

- 4 ^{TTP}Powerlinks/^{TTP}Powernodes on a rack
- 1 ^{TTP}Monitoring Node with housing and power supply
- Dimensions: 132 x 240.7 x 341 (in mm)
- Weight: 6.000 g
- Power supply: max. 100 W
- Supply voltage: 110 to 240 V, 50 to 60 Hz

Software Licenses

^{TTP}Development Cluster includes single-seat licenses for the following software:

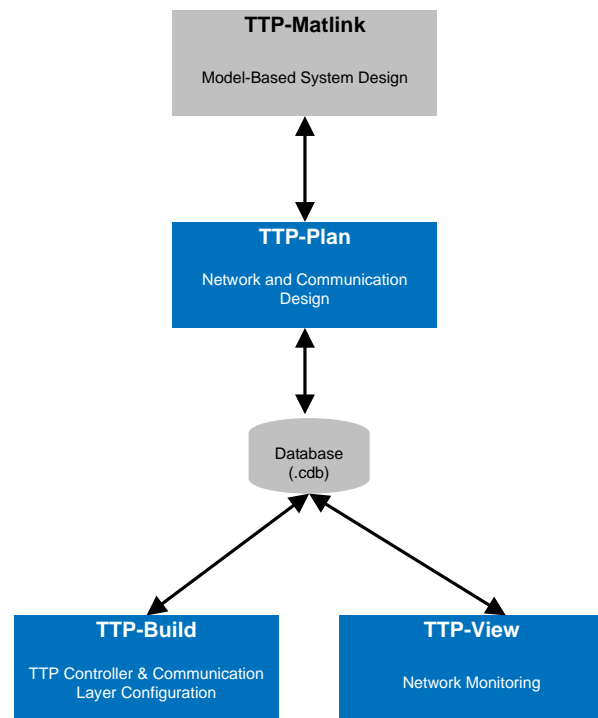
- ^{TTP}Plan and ^{TTP}Build
- ^{TTP}OS (development licenses)
- ^{TTP}Load and ^{TTP}View
- Demonstration software
- P&E PowerPC Flash/EEPROM Programmer for Freescale MPC555/8xx with cable

System Requirements

- Standard PC with Windows XP; 1.5 GHz or above; 1 GB RAM
- Wind River Compiler (only evaluation version of WindRiver compiler included)
- Lauterbach Debugger (recommended)

Related Products

The ^{TTP}Matlink blockset provides full integration of the ^{TTP}Tools in Matlab/Simulink and allows system level modeling and simulation of the network.



Order Numbers

- H01.07.1: ^{TTP}Development Cluster with 4 ^{TTP}Powerlinks
- H01.03.2: ^{TTP}Development Cluster with 4 ^{TTP}Powernodes

Optional Products & Services

- M01.03.2: 1 year software maintenance service
- H0x.0x.x ^{TTP}Matlink; MATLAB/Simulink blockset
- H20.04.3: Additional ^{TTP}Powerlink
- H09.02.2: Additional ^{TTP}Powernode

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